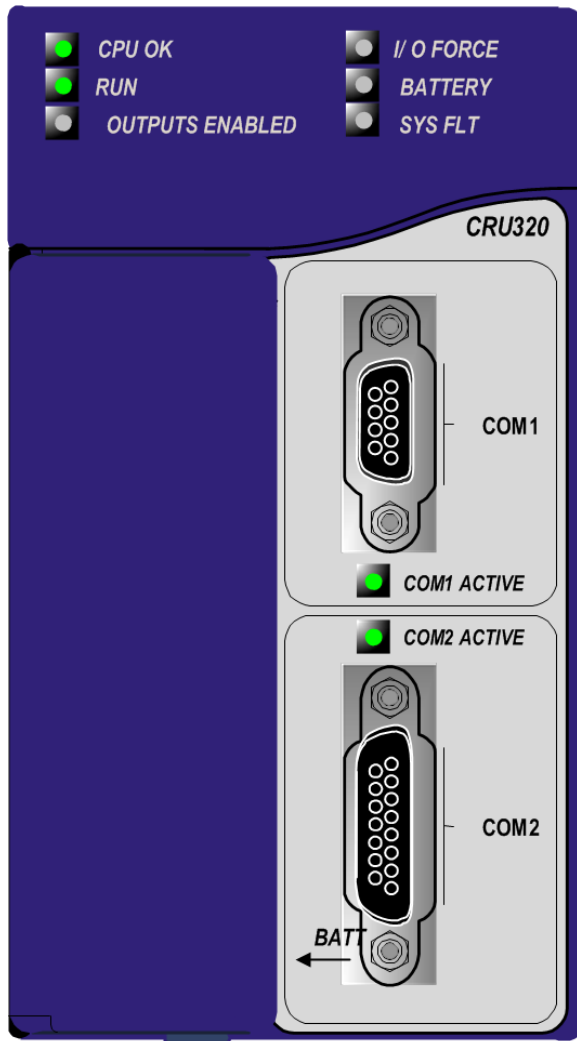


GFK-2514AD  
June 2016



The PACSystems\* RX3i Redundancy CPU can be used to perform real time control of machines, processes, and material handling systems. The CPU communicates with the programmer and HMI devices via a serial port using SNP Slave protocol. It communicates with I/O and smart option modules over a dual backplane bus that provides:

- High-speed PCI backplane for fast throughput of new advanced I/O.
- Serial backplane for easy migration of existing Series 90\*-30 I/O.

### Features

- Hot standby (HSB) redundancy. Two redundant units make up a redundancy system. Each unit requires one Redundancy CPU (IC695CRU320) and a redundancy Memory Xchange module (IC695RMX128/228) configured as a redundancy link.
- Contains 64 Mbytes of battery-backed user memory and 64 Mbytes of non-volatile flash user memory.
- Provides access to bulk memory via reference table %W.
- Configurable data and program memory.
- Programming in Ladder Diagram, Structured Text, Function Block Diagram, and C. Refer to *PACSystems RX7i & RX3i CPU Programmer's Reference Manual, GFK-2950*.
- Supports auto-located Symbolic Variables that can use any amount of user memory.
- Reference table sizes include 32Kbits for discrete %I and %Q and up to 32K words each for analog %AI and %AQ.
- Supports most Series 90-30 modules and expansion racks. For a list of supported I/O, Communications, Motion, and Intelligent modules, refer to the *PACSystems RX3i System Manual, GFK-2314F* or later.
- Supports up to 512 program blocks. Maximum size for a block is 128KB.
- CPU firmware may be upgraded in the field.
- CPU supports firmware upgrades of modules in its backplane.
- Two serial ports: an RS-485 serial port and an RS-232 serial port.
- Ethernet communications via the rack-based Ethernet Interface module (IC695ETM001). For details on Ethernet capabilities, refer to *PACSystems RX7i & RX3i TCP/IP Ethernet Communications User Manual, GFK-2224*.
- Time Synchronization to SNTP Time Server on Ethernet network when used with Ethernet Release 5.0 or later.
- Compliant with EU RoHS Directive 2002/95/EC using the following exemptions identified in the Annex: 7(a), 7(c)-I, & 7(c)-III.

\* Indicates a trademark of General Electric Company and/or its subsidiaries. All other trademarks are the property of their respective owners.

### Ordering Information

Catalog Number	Description
IC695CRU320	RX3i 1GHz Celeron M CPU with 64MB User Memory
IC695ACC302	Smart Auxiliary Battery for Memory Retention
IC693ACC302	Auxiliary Battery for Memory Retention
IC690RBT001	Rechargeable Battery for Memory Retention



**Warning** – Use only GE approved auxiliary battery packs listed above. Use of another battery may present a risk of fire or explosion.

### Hot Standby CPU Redundancy Features

For details on the configuration and operation of a Hot Standby CPU redundancy system, refer to the *PACSystems Hot Standby CPU Redundancy User's Manual*, GFK-2308.

- Supports single and redundant Ethernet remote I/O LANs through Ethernet Network Interface Unit (ENIU) modules
- Supports simplex and redundantly controlled PROFINET remote IO (requires firmware version 8.00 or later)
- Survives any one single point of failure
- Bumpless switching
  - Synchronized CPUs
  - One scan switching
  - Transfer data size up to 2Mbytes; selected in CPU hardware configuration and in variable properties
- Supports two redundancy communications links
- Online repair of failed component
- Online programming
- Redundancy Memory Xchange Module
  - Manual toggle switch for role switching, which transitions control from the active unit to the backup unit
  - Redundancy status LEDs
- Application-initiated role switching to switch the active unit to backup status
- Redundancy status bits and message logging
- Memory error checking and correction (ECC) single bit correcting and multiple bit checking
- Background diagnostics

### HSB Control Strategy

The HSB control strategy has the following characteristics:

- Active unit does not automatically switch to primary on resynchronization
- Critical control data plus all redundant outputs must be included in the output data transfer
- Bumpless switchover from active unit to backup unit

## Specifications: CRU320

<b>CPU Performance</b>	For CRU320 performance data, refer to Appendix A of the <i>PACSystems CPU Reference Manual</i> , GFK-2222W or later.
<b>Battery: Memory retention</b>	For battery selection, installation and estimated life, refer to the <i>PACSystems RX3i and RX7i Battery and Energy Pack Manual</i> , GFK-2741
<b>Program storage</b>	Up to 64 MB of battery-backed RAM 64 MB of non-volatile flash user memory
<b>Power requirements</b>	+3.3 Vdc: 1.0 Amps nominal +5 Vdc: 1.2 Amps nominal
<b>Operating Temperature</b>	0 to 60°C (32°F to 140°F)
<b>Floating point</b>	Yes
<b>Time of Day Clock accuracy</b>	Maximum drift of 2 seconds per day
<b>Elapsed Time Clock (internal timing) accuracy</b>	0.01% maximum
<b>Embedded communications</b>	RS-232, RS-485
<b>Serial Protocols supported</b>	Modbus RTU Slave, SNP, Serial I/O
<b>Backplane</b>	Dual backplane bus support: RX3i PCI and high-speed serial bus
<b>PCI compatibility</b>	System designed to be electrically compliant with PCI 2.2 standard
<b>Program blocks</b>	Up to 512 program blocks. Maximum size for a block is 128KB.
<b>Memory</b>	%I and %Q: 32Kbits for discrete %AI and %AQ: configurable up to 32Kwords %W: configurable up to the maximum available user RAM Symbolic: configurable up to 64 Mbytes
<b>Flash memory endurance rating</b>	100,000 write/erase cycles minimum
<b>Memory error checking and correction (ECC)</b>	Single bit correcting and multiple bit checking.
<b>Switchover Time<sup>1</sup></b>	Maximum 1 logic scan, minimum 3.133 msec.
<b>Typical Base Sweep Time (Reference Data Transfer List Impact)<sup>2</sup></b>	3.66 msec: 1K Discrete I/O, 125 Analog I/O and 1K Registers 3.87 msec: 2K Discrete I/O, 250 Analog I/O and 2K Registers 4.30 msec: 4K Discrete I/O, 500 Analog I/O and 4K Registers 5.16 msec: 8K Discrete I/O, 1K Analog I/O and 8K Registers
<b>Maximum amount of data in transfer list</b>	Up to 2 Mbytes
<b>Number of redundancy links supported</b>	Up to two IC695RMX128/228 synchronization links are supported.

For product standards, general operating specifications, and installation requirements, refer to *PACSystems RX3i System Manual*, GFK-2314.

<sup>1</sup> Switchover time is defined as the time from failure detection until backup CPU is active.

<sup>2</sup> Symbolic variable and Reference data can be exchanged between controllers. Up to 2 Mbyte of data is available for transfer

## Installation Location

This product is intended for use with the RX3i system. Its components are considered open equipment (having live electrical parts that may be accessible to users) and must be installed in an ultimate enclosure that is manufactured to provide safety. At a minimum, the enclosure shall provide a degree of protection against solid objects as small as 12mm (fingers, for example). This equates to a NEMA/UL Type 1 enclosure or an IEC60529 IP20 rating providing at least a pollution degree 2 environment. For details about installing RX3i rack systems, refer to *PACSystems RX3i System Manual*, GFK-2314.

## Installation in Hazardous Areas

The following information is for products bearing the UL marking for Hazardous Areas or ATEX marking for explosive atmospheres:

### CLASS 1 DIVISION 2 GROUPS ABCD

- This equipment is an open-type device and is meant to be installed in an enclosure suitable for the environment that is only accessible with the use of a tool.
- Suitable for use in Class I, Division 2, Groups A, B, C and D Hazardous Locations, or nonhazardous locations only.



**Warning** – EXPLOSION HAZARD - SUBSTITUTION OF COMPONENTS MAY IMPAIR SUITABILITY FOR CLASS I, DIVISION 2.



**Warning** – WHEN IN HAZARDOUS LOCATIONS, TURN OFF POWER BEFORE REPLACING OR WIRING MODULES.



**Warning** – EXPLOSION HAZARD - BATTERIES MUST ONLY BE CHANGED IN AN AREA KNOWN TO BE NON-HAZARDOUS.

### ATEX Zone 2

This module must be mounted in an enclosure certified in accordance with EN60079-15 for use in Zone 2, Group IIC and rated IP54. The enclosure shall only be able to be opened with the use of a tool.

## Release History

Catalog Number	Firmware Revision	Date	Comments
IC695CRU320-EZ	8.95	June 2016	Adds support for the <i>Remote Get HART Device Information COMMREQ</i> . This allows the user application to retrieve information from a HART device connected to an RX3i Analog module via an IC695PNS001 RX3i PROFINET Scanner (PNS).
IC695CRU320-EY	8.75	Apr 2016	Several security enhancements were made to various PACSystems Controllers to harden the product against an attacker with local area network access. Refer to <a href="https://ge-ip.force.com/communities/articles/en_US/Article/Security-Enhancements?Type=Article">https://ge-ip.force.com/communities/articles/en_US/Article/Security-Enhancements?Type=Article</a>
IC695CRU320-EX	8.70	Nov 2015	Supports extended PROFINET device Subslot Numbers in order to support additional 3 <sup>rd</sup> party devices. The maximum Subslot Number for PROFINET devices has been increased from 255 to 21845.
IC695CRU320-EW	8.50	May 2015	Add support for HART® Pass Through feature. Refer to <i>PACSystems RX3i HART Pass Through User Manual</i> , GFK-2929.
IC695CRU320-EV	8.40	Jan 2015	The CRU320 now supports 255 PROFINET I/O Devices in a PACSystems RX3i Hot Standby CPU Redundancy system. PROFINET operation uses the PROFINET I/O Controller module IC695PNC001. Redundant operation conforms to PROFINET V2.3 Type S-2 System Redundancy.  PROFINET I/O operation in a HSB CPU Redundancy system is described in the following user manuals: <ul style="list-style-type: none"> <li>• <i>PACSystems Hot Standby CPU Redundancy User's Manual</i>, GFK-2308K</li> <li>• <i>PACSystems RX3i PROFINET IO Controller User Manual</i>, GFK-2571D</li> </ul>

Catalog Number	Firmware Revision	Date	Comments
IC695CRU320-EU	8.15	May 2014	Adds support for IC695RMX228 128 MB Reflective Memory Module with Single Mode Transceiver. Adds ability to read reflective memory status bits for IC695CMX128, IC695RMX128, and IC695RMX228 (reflective memory modules). Resolves issue of Serial I/O Read Bytes COMMREQ (4402) always returning error code 100Dh in the event of an error, regardless of the error.
IC695CRU320-ET	8.05	Feb 2014	Adds support for the new IC695ECM850 (IEC 61850 Ethernet Communication Module), which operates as an IEC 61850 Client and provides connectivity to IEC 61850 Server devices. Resolves issue of CPU halting when the inner FOR_LOOP's Start is greater than End input in a nested FOR_LOOP.
IC695CRU320-ES	8.00	Dec 2013	Adds remote PROFINET IO to PACSystems RX3i Hot Standby Redundancy systems. Resolves issue with LREAL operands within complex relational expressions.
IC695CRU320-ER	7.81	Oct 2013	Resolves an issue where the CRU320 would go into halt mode when downloading to a CRU320 (Firmware 7.70 and later) that has GBCs in the configuration and the partner CRU320 is not configured.
IC695CRU320-EP	7.80	Sep 2013	Resolves the three issues listed in <i>PACSystems RX3i CPU IC695CRU320 IPI</i> , GFK-2514T.
IC695CRU320-EN	7.75	May 2013	Corrects an issue in versions 7.15 – 7.70 that required setting OEM lock in Enhanced Security in order to preserve the OEM lock through a power cycle. Adds support for RX3i CMX/RMX modules version –CG (hardware version Cx with firmware version 2.00 and later).
IC695CRU320-EM	7.70	Feb 2013	Adds support for the following new modules: IC694MDL758 and IC695CNM001.
IC695CRU320-EL	7.18	Nov 2012	Adds the ability to monitor a new interface between the CPU and the CMX/RMX Memory Xchange modules (CMX/RMX Memory Xchange module firmware 1.06 and later). In the rare occasion the CMX/RMX Memory Xchange module detects a failure in a read operation, the CMX/RMX Memory Xchange module will indicate the last read is invalid. The CPU will then retry the previous read operation.
IC695CRU320-EK	7.17	Oct 2012	Adds the ability to monitor the new queue alignment flag provided by CMX/RMX Memory Xchange modules with firmware version 1.05 or greater. If a queue alignment problem is detected, the CPU retries the read operation where the problem occurred.
IC695CRU320-EJ	7.15	July 2012	Adds native support for the IC694PSM001 module and resolves several issues. Also introduces new features to augment security in the CPU firmware and Proficy Machine Edition software. For details, see <i>PACSystems RX3i CPU IC695CRU320 IPI</i> , GFK-2514N
IC695CRU320-EH	7.14	Jun 2012	Corrects the issue described in <i>PACSystems RX3i CPU IC695CRU320 IPI</i> , GFK-2514M.
IC695CRU320-EH	7.13	Apr 2012	Corrects issues with <i>Logic Driven Write to Flash</i> (Service Request 57) and Run mode store in HSB redundancy systems with synchronized CPUs.
IC695CRU320-EG	6.72	Aug 2011	Corrects an issue with the RS-485 (COM2) serial port.
IC695CRU320-DF	6.71	May 2011	Corrects the behavior of the <i>Logic Driven Read/Write to Flash</i> service requests, SVC_REQ 56 and SVC_REQ 57.
IC695CRU320-DE	6.70	Mar 2011	Implements a hardware design update that improves the noise immunity of the CPU module during power up from flash operations.
IC695CRU320-CE	6.70	Dec 2010	For features introduced and problems resolved, see <i>PACSystems RX3i CPU IC695CRU320 IPI</i> , GFK-2514G.
IC695CRU320-CD	6.02	Aug 2010	Provides capability for low battery detection. The new hardware is EU-ROHS compliant. For details, see <i>PACSystems RX3i CPU IC695CRU320 IPI</i> , GFK-2514F.
IC695CRU320-BC	6.01	Mar 2010	Provides for OEM protection in flash-based systems that do not use a battery. For additional problems resolved, see <i>PACSystems RX3i CPU IC695CRU320 IPI</i> , GFK-2514E.
IC695CRU320-BB	6.00	May 2010	See <i>PACSystems RX3i CPU IC695CRU320 IPI</i> , GFK-2514D for new features and problems resolved.
IC695CRU320-BA	5.70	July 2009	Hardware-only upgrade to enhance manufacturability. Does not affect product features or functional compatibility.
IC695CRU320-AA	5.70	Feb 2009	Initial release.

## Important Product Information for this Release

### Field Upgrade

**Upgrade Kit:** 82A1559-SW01-001-A23

When upgrading for the *Remote Get HART Device Information COMMREQ* functionality, the supporting Analog I/O modules must contain HART-compatible firmware. The PROFINET Controller (PNC001) and PROFINET Scanner (PNS001) must also contain HART-compatible firmware (see below).

### New Features and Enhancements

Subject	Description
<b><u>Remote Get HART Device Information COMMREQ</u></b>	<p>Information from a HART device connected to an RX3i Analog Module in an IC695PNS001 RX3i PROFINET Scanner may be read into the user application using the <i>Remote Get HART Device Information COMMREQ</i>. This COMMREQ is supported by the IC695PNC001 PROFINET I/O Controller module.</p> <p><i>Remote Get HART Device Information COMMREQ</i> operation is described in GFK-2314 <i>PACSystems RX3i System Manual</i>.</p> <p>A UDFB that automates the COMMREQ control logic is available for download from GE's Automation &amp; Controls support website. (Article ID: 000017816) <a href="https://ge-ip.force.com/communities/en_US/Download/Remote-Get-HART-Device-Information-COMMREQ-UDFB">https://ge-ip.force.com/communities/en_US/Download/Remote-Get-HART-Device-Information-COMMREQ-UDFB</a></p>

### Problems Resolved by this Revision

Subject	ID Code	Description
<b>EGD Production Does not Start on Power Up</b>	DE3368	In rare cases EGD production may not start when the CPU powers on and goes to Run Mode.
<b>IC695GCG001 Does not Scan Genius I/O After Power Up</b>	US15709 SFDC00345 603	In an RX3i Hot Standby Redundancy system using IC695GCG001 RX3i Genius Communications Gateways, the GCG001 connected to the secondary controller does not scan Genius I/O after powering on. This occurs if the primary controller is off and both the secondary GCG001 and secondary controller are powered on at the same time.

### Functional Compatibility

The following are required to use the features introduced in the most recent CPU release:

Subject	Minimum Version Required
<b>Programmer Version Requirements</b>	PME 8.50 SIM 2 or later.
	Extended PROFINET device Subslot Number range – PME 8.60 SIM8 or later.
<b>Remote Get HART Device Information COMMREQ</b>	<p>The <i>Remote Get HART Device Information COMMREQ</i> is supported by these products:</p> <ul style="list-style-type: none"> <li>• RX3i CPUs with version 8.95 or later</li> <li>• IC695PNC001 RX3i PROFINET Controller version 2.26 or later</li> <li>• IC695PNS001 RX3i PROFINET Scanner version 2.41 or later</li> <li>• IC695PNS001 RX3i PROFINET Scanner GSDML-V2.3-GEIP-RX3iPNS-20160602.xml<sup>3</sup></li> </ul>
<b>Supports the HART Pass Through Feature</b>	HART-capable RX3i Analog I/O modules can communicate HART data via this CPU to compatible asset management tools. Refer to the <i>PACSystems HART Pass Through User Manual</i> , GFK-2929 for more details.

<sup>3</sup> IC695PNS001 RX3i PROFINET Scanner GSDML-V2.3-GEIP-RX3iPNS-20160602.xml is required to enable HART Pass Through Service Options settings in PME.

Subject	Minimum Version Required
	<p>The following RX3i analog modules support HART:</p> <p>IC695ALG626                      IC695ALG628<sup>4</sup>                      IC695ALG728</p> <p>If used for HART Pass Through, the supporting RX3i PROFINET Controller (PNC001) and PROFINET Scanner (PNS001 or CEP001) must also contain HART-compatible firmware:</p> <p>IC695PNC001 Firmware Release 2.20                      IC695PNS001 Firmware Release 2.30                      IC695CEP001 Firmware Release 2.30.</p>
<p><b>C Toolkit Compatibility</b></p>	<p>The C Toolkit for PACSystems is distributed with Proficy Machine Edition Logic Developer. Updates can be downloaded from <a href="http://www.ge-ip.com/support">http://www.ge-ip.com/support</a>.</p> <p>C Toolkit Release 5.50, distributed with Machine Edition Logic Developer 5.9 or later, is required for use with the CRU320.</p> <p><b>Note:</b> C blocks that were built using C Toolkit versions earlier than 5.00 Build 16C1 must be recompiled using a newer toolkit version for use with CPU firmware release 5.00 or higher.</p> <p><b>Note:</b> The Series 90 Toolkit (IC641SWP709/719) is not compatible with PACSystems.</p>
<p><b>RS-485 Port Compatibility</b></p>	<p>When the CPU is first powered on, the RS-485 port (COM 2) powers up with the transmitter enabled. The transmitter is placed into a high-impedance state once the CPU OK LED is illuminated. Since that takes a finite amount of time, this could be an issue if the COM 2 port is being used in multi-drop communications, and other devices share the same cable via wired-OR connections. If one of those devices is actively communicating when the CPU is powered up, there is a potential for those communications to be disrupted until the CPU puts the RS-485 port into high-impedance state.</p>
<p><b>Low Battery Detection</b></p>	<p>For the low battery detection circuit to function properly, a <i>Smart Battery</i> must be used. For details, refer to the <i>PACSystems RX3i and RX7i Battery and Energy Pack Manual</i>, GFK-2741</p>
<p><b>Rx3i Backplane Hardware Revision</b></p>	<p>For the CRU320 CPU, one of the following backplane hardware revisions MUST be used:</p> <p>IC695CHS012-BAMP                      IC695CHS016-BAMP                      IC695CHS012CA-BAMP                      IC695CHS016CA-BAMP</p> <p>or</p> <p>IC695CHS012-CA (or later)                      IC695CHS016-CA (or later)                      IC695CHS012CA-CA (or later)                      IC695CHS016CA-CA (or later)</p>
<p><b>Motion Module not supported by Redundancy CPU</b></p>	<p>The CRU320 does not support the PMM335 PACMotion Multi-Axis Motion Controller.</p>
<p><b>Backplanes, power supplies and I/O modules</b></p>	<p>For lists of compatible backplanes, power supplies and I/O modules, refer to the <i>PACSystems RX3i System Manual</i>, GFK-2314F or later.</p>
<p><b>Series 90-30 Expansion Rack Compatibility</b></p>	<p>Series 90-30 expansion racks, both local and remote, are supported by the PACSystems RX3i System. PACSystems RX3i CPUs must be located in an RXi Universal Backplane. They do not operate in a Series 90-30 Rack.</p>
<p><b>Series 90-30 Main Rack Compatibility</b></p>	<p>Series 90-30 Main Racks cannot be used in a PACSystems RX3i system. Series 90-30 CPUs do not operate in PACSystems RX3i Racks.</p>
<p><b>COMMREQ to PBM300</b></p>	<p>In Release 3.0, the behavior of the COMMREQ fault output on a COMMREQ sent to the PROFIBUS master module IC695PBM300 was changed to be compatible with the Series 90-30 CPU366 PROFIBUS Master. Previously, the fault output is enabled when the module receives a COMMREQ and it is busy. Now, the busy condition does not result in the fault output enabled.</p>
<p><b>Recommended IC200ALG240 revision</b></p>	<p>When a VersaMax* system Genius* Network Interface Unit (IC200GBI001) operates with a Genius Bus Controller located in a PACSystems RX3i, and the VersaMax system contains an IC200ALG240 Analog Input Module, it is recommended to update the IC200ALG240 firmware to Revision 1.20 or later using upgrade kit 44A752313-G02 or later. Upgrade kits are available at <a href="http://www.ge-ip.com/support">http://www.ge-ip.com/support</a>.</p>

<sup>4</sup> If used, IC695ALG628 must be installed in the RX3i CPU Rack. At time of publication, it is not supported by PROFINET scanners IC695PNS001 or IC695CEP001. Refer to IPIs for IC695PN001 or IC695CEP001 for future updates.

<i>Subject</i>	<i>Minimum Version Required</i>
<b>Redundancy operation with dissimilar CPU models</b>	Not allowed: The CRU320 does not support synchronization with RX7i redundancy units.
<b>PNC001 Version for Extended PROFINET Subslot Numbers</b>	IC695PNC001 PROFINET Controller firmware version 2.25 or later is required to configure PROFINET devices using extended subslot numbers.
<b>CPE330 / CRU320 Mixed model Dual Synchronization</b>	When a CPE330 and CRU320 are operating in a mixed-model redundancy system, Dual Synchronization at power-up cannot be guaranteed because of the power up time differences of the two CPUs.

## Restrictions and Open Issues

### Restrictions and Open Issue

<i>Restriction/Open Issue</i>	<i>Description</i>
<b>EGD Producer Exchanges should be allowed without Redundant IP and/or Produce in Backup Mode</b>	<p>With Proficy Machine Edition 8.60 SIM8 and earlier, if Producer Exchanges are configured on any RX3i Redundant system a download to the controller may fail with the following error message:</p> <p>"Error 8394: EGD producers in redundancy system must use redundant IP address, unless EGD is also produced in backup mode. [HWC Location: R.S.U, where R = Rack #, S = Slot #, and U = Subslot #]"</p> <p><b>Solution:</b> Set the Redundant IP Address parameter or Produce in Backup Mode depending on your configuration using the following suggestions.</p> <ul style="list-style-type: none"> <li>• The Redundant IP feature should only be enabled on Ethernet interfaces that are used for general communication, like e.g. HMI.</li> <li>• The Redundant IP should not be enabled on Ethernet interfaces that service Remote I/O. Instead, to send the same exchanges from active-, and backup, the following should be considered: <ul style="list-style-type: none"> <li>○ In non-HSB systems, each controller has a unique Local Producer ID and can therefore send its own unique exchanges, regardless of active-, or backup mode. Exchange IDs between active-, and backup unit may even be identical.)</li> <li>○ In HSB systems, Produce in Backup Mode should be set to True. Both controllers share the same Local Producer ID, but the Secondary Produced Exchange Offset allows to send the same exchanges from both PLCs.)</li> </ul> </li> </ul> <p>If however the Redundant IP is enabled on a pair of Ethernet interface that services Remote I/O, EGD exchanges sent to the PLCs should not be directed to the RedundantIP. -The backup unit will not consume exchanges on the Redundant IP. Instead, the exchanges should be broadcast, so both controllers can receive them.</p>
<b>Missing Addition of IOC event when ECM850 module restarts due to reset pushbutton or SVC_REQ 24</b>	When ECM850 module RESET is triggered using SVC_REQ 24 or Reset pushbutton, CPU does not report <i>Addition of IOC</i> fault message in Controller fault table after successful reset of module.
<b>Rare condition of CPU lights out during power cycles</b>	In rare instances during multiple rapid power cycles, the CPU will fail to power up. If this occurs, the CPU should recover with the next power cycle.
<b>The Ethernet module fails to exchange EGD properly during power cycling</b>	<p>Very rarely, after experiencing multiple rapid power cycles, the CPU may fail to establish communication with one or more modules in the backplane at power up. When this occurs, several pairs of a <i>Loss of, or missing option module</i> and <i>Reset of option module</i> faults will be logged in the controller fault table.</p> <p>If the module is an ETM, an event 30H is recorded in its station manager event log.</p> <p>To recover from this issue, cycle power again.</p>



**Restrictions and Open Issue**

<b>Restriction/Open Issue</b>	<b>Description</b>
<b>Loss of power supplies after firmware update</b>	A Loss of Power Supplies after firmware update may occur. This does not happen with all firmware updates and will NOT occur if the system is power cycled after the firmware upgrade has completed. The faults displayed when this issue occurs are as follows: Loss of, or missing option module Error Code: 36 Group: 4 Action: 3:Fatal Task Num: 9 Fault Extra Data: 01 58 02 4f 80 08 0a 07 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
<b>Battery installation.</b>	When installing a new battery, if there currently is no battery installed, the battery must be installed while the CPU has power. Failing to follow this procedure could result in the CPU not powering up. If a battery is installed while power is off (and there was no battery previously installed), and the CPU fails to power up, remove the battery, power cycle the CPU and then install the battery.
<b>Hot swapping some analog modules slowly may result in modules not being recognized.</b>	Occasionally during a hot insertion (hot swap) of RX3i Non-Isolated Analog Input Modules, input channels may take up to 2 seconds to reflect actual input values after the module OK bit is enabled in the module status word. This has only been seen when the hot insertion has been done slowly (i.e. approximately 1.5 seconds to insert the module). Refer to PACSystems RX3i System Manual, GFK-2314F or later for updated <i>hot-swap</i> information.
<b>Ethernet disconnect during Word-for-Word change.</b>	If the Ethernet connection is broken during a word-for-word change, the programmer may not allow a subsequent word-for-word change after reconnecting due to the fact that it thinks another programmer is currently attached. If this occurs, you should go offline and then back online again.
<b>Simultaneous Clears, Loads and Stores not supported.</b>	PACSystems CPUs do not support multiple programmers changing CPU contents at the same time. The programming software may generate an error during the operation. Simultaneous loads from a single CPU are allowed.
<b>Hardware configuration Not Equal after changing target name.</b>	If you store a hardware configuration to flash that sets Logic/Config Power-up Source to <i>Always Flash</i> or <i>Conditional Flash</i> and then change the name of the target in the programming software, the hardware configuration will indicate <i>Not Equal</i> and will not verify as <i>Equal</i> .
<b>Controller and IO Fault Tables may need to be cleared twice to clear faulted state.</b>	Both Controller and IO fault tables may need to be cleared to take the CPU out of Stop/Fault mode. If one of the tables contains a recurring fault, the order in which the tables are cleared may be significant. If the CPU is still in Stop/Fault mode after both tables are cleared, try clearing the fault tables again.
<b>Setting Force On/Off by storing initial value.</b>	Once a force on or force off has been stored to the controller, you cannot switch from force on to force off or vice-versa by downloading initial values. To turn the force on or off, download the project.
<b>Number of active programs returned as zero.</b>	The SNP request Return Controller Type and ID currently returns the number of active programs as zero.
<b>Serial I/O fails at 115K during heavy interrupt load.</b>	Rare data corruption errors have been seen on serial communications when running at 115K under heavy interrupt load on the controller. Under heavy load applications, users should restrict serial communications to 57K or lower.
<b>Multi-drop serial communications interrupted by CRU320 power cycle</b>	Communications can be disrupted when two EIA485 ports (COM 2) are wired together/multi-dropped using a two wire connection (wired OR) and either CPU is powered on while the other CPU is communicating.
<b>SNP ID not always provided.</b>	Unlike the Series 90-30, the RX3i CPU's SNP ID does not appear in the Machine Edition programmer Show Status display. Service Request 11 will always return zeros.
<b>Second programmer can change logic while in Test &amp; Edit mode.</b>	While currently active in a Test and Edit session using Machine Edition on one PC, Machine Edition running on another PC is not prevented from storing new logic to the controller.
<b>Must have logic if powering up from flash.</b>	If the application will configure the CPU to retrieve the contents of flash memory at power-up, be sure to include logic along with hardware configuration when saving to flash memory.

**Restrictions and Open Issue**

<b>Restriction/Open Issue</b>	<b>Description</b>
<b>CPU may not detect low-battery condition</b>	An IC693ACC302 battery with very low capacity may still have a terminal voltage high enough to report that it is a good battery. In this case, when the battery starts supplying the memory power (battery backup), the battery voltage quickly drops to unacceptable levels, with little warning to the user before failure. To insure against data loss, users should replace batteries in accordance with the guidelines provided in the <i>PACSystems RX7i &amp; RX3i CPU Reference Manual</i> , GFK-2222. Additionally, users could save logic and hardware configuration to flash.
<b>Two Loss of Module faults for Universal Analog module.</b>	Occasionally, the hot removal of the Universal Analog Input Module (IC695ALG600) results in two <i>Loss of I/O Module</i> faults instead of one.
<b>Power up of Series 90-30 HSC module may take as long as 20 seconds.</b>	As power is applied to a 90-30 High-Speed Counter, the Module Ready bit in the status bits returned each sweep from the module may not be set for as long as 20 seconds after the first controller sweep, even though there is no Loss of Module indication. I/O data exchanged with the module is not meaningful until this bit is set by the module. Refer to the <i>Series 90-30 High Speed Counter User's Manual</i> , GFK-0293C, Section 4.7.
<b>Informational fault at power up.</b>	Intermittently during power-up, an Informational non-critical CPU software fault may be generated with fault extra data of 01 91 01 D6. This fault will have no effect on the normal operation of the controller. But, if the hardware watchdog timer expires after this fault and before power has been cycled again, then the outputs of I/O modules may hold their last state, rather than defaulting to zero.
<b>Extended memory types for IO triggers.</b>	%R, %W and %M cannot be used as IO triggers.
<b>Possible Machine Edition inability to connect.</b>	Infrequently, an attempt to connect a programmer to a controller via Ethernet will be unsuccessful. The normal connection retry dialog will not be displayed. Rebooting the computer that is running the programmer will resolve the behavior.
<b>SNP Update Datagram message.</b>	If an Update Datagram message requests 6 or less bits or bytes of data, the controller will return a Completion Ack message without Text Buffer. The protocol specifies that the returned data will be in the Completion Ack message, but it may not be.
<b>GBC30 may not resume operation after power cycle.</b>	In rare instances, a GBC30 in an expansion rack may not resume normal operation after a power cycle of either the expansion rack or the main rack. To restore GBC operation, power cycle the rack again.
<b>Configuration of third-party modules.</b>	Do not specify a length of zero in the configuration of a third-party module. The module will not work properly in the system.
<b>Power supply status after CPU firmware update.</b>	The controller will report a Loss of or Missing Option Module fault for the IC695PSD140 RX3i power supply following an update of CPU firmware. Also, the slot will appear empty in the programmer's online status detail view. The power supply continues to operate normally. Power cycle to restore normal status reporting.
<b>Power supply status after power cycling.</b>	Rarely, turning a power supply on or off may fail to result in an <i>Add</i> or <i>Loss</i> fault. Also, the slot will appear empty in the programmer's online status detail view. The power supply continues to operate normally. Power cycle to restore normal status reporting.
<b>Don't use multiple targets.</b>	In a system in which the hardware configuration is stored from one target and logic is stored from a different target, powering-up from flash will not work. The observed behavior is that, following a power up from flash, Machine Edition reports hardware configuration and logic <i>Not Equal</i> .
<b>Missing Loss of Terminal Block fault.</b>	The IC695ALG600/608/616 analog input modules do not produce a <i>Loss of Terminal Block</i> fault when hardware configuration is stored or the module is hot-inserted, and the terminal block is not locked into place.
<b>Sequence Store failure.</b>	When downloading projects with very large hardware configuration or which use large amounts of user memory, it is possible to encounter a controller Sequence Store Failure error when writing the project to flash. To work around this error, either or both of the following actions may be helpful: <ol style="list-style-type: none"> <li>1. Perform an explicit clear of flash prior to performing the write.</li> <li>2. Increase the operation timeout used by Machine Edition prior to performing the write. This is done by expanding the Additional Configuration in the Inspector window for the target controller, and adjusting the Request Timeout. The timeout may need to be increased to as much as 60,000 msec, depending on the amount of memory used and the condition of the flash memory.</li> </ol>

**Restrictions and Open Issue**

<b>Restriction/Open Issue</b>	<b>Description</b>
<b>IC693/IC694MDL754: Must configure 16 module status bits</b>	Always configure sixteen bits of module status when using module MDL754. Configuring zero bits of module status will result in invalid data in the ESCP status bits of the module.
<b>IC695ALG600 Lead Resistance Compensation setting.</b>	A configuration store operation will fail if a channel is configured for 3-wire RTD and Lead Resistance Compensation is set to Disabled. A Loss of Module fault will be logged in the I/O Fault table at the end of the store operation. To recover the lost module, the configuration must be changed to enable Lead Resistance Compensation and module must be power cycled.
<b>WinLoader may stop operating.</b>	On computers running Windows 2000 and using some versions of Symantec Antivirus protection, WinLoader will lock up if used in advanced mode. Recovery requires cycling the computer's power.
<b>Logic and HWC Not Equal after power cycle.</b>	If the Hardware Configuration from Target 1, with Logic/Configuration Power-up Source and Data Source both set to <i>Always from Flash</i> , is stored in Flash, then Logic and Hardware Config from Target 2, with Logic/Configuration Power-up Source both set to <i>Always from RAM</i> , are stored to RAM and there is a good battery, then when power is cycled the programmer may show that Logic and Hardware Config are <i>Not Equal</i> . The remedy is to clear Flash and re-store the Logic and Hardware Config from Target 2.
<b>WinLoader does not detect PC COM port in use when upgrading PACSystems CPU.</b>	WinLoader does not detect if a PC's COM port is in use when attempting to connect to a PACSystems CPU to perform a firmware upgrade. If the port is already in use it displays the status <i>trying to connect</i> followed by <i>waiting for target</i> . To proceed with the upgrade, press the <i>abort</i> button and disconnect the other application that is using the COM port.
<b>CPU320, CRU320 user application and values cleared after power cycle.</b>	Under rare circumstances during multiple rapid power cycles the CPU320/CRU320 will power up with the user application and data in RAM cleared. There will be a <i>Corrupted user memory</i> fault in the controller fault table (Group 130, Error code 1). This will not occur if the user application and data are loaded from flash on power-up ( <i>Always Flash</i> or <i>Conditional Flash</i> )
<b>WinLoader does not display error when it can't connect serially with PACS CPU.</b>	WinLoader does not display an error message if it cannot connect to the PACS CPU when attempting to connect to a PACSystems CPU to perform a firmware upgrade. This occurs if the cable is physically not connected to the CPU or if the CPU's serial port is not configured for the same baud as WinLoader. In this case Winloader displays the status <i>trying to connect</i> followed by <i>waiting for target</i> . To proceed with the upgrade, press the <i>abort</i> button and correct the cable or baud rate setting.

## Operational Notes

### Operational Notes

Subject	Description
<b>Firmware upgrades using Slot 1</b>	Firmware upgrades for modules in Slot 1 will only work for CPUs. Modules other than the CPU need to be in Slot 2 or higher to perform a firmware upgrade.
<b>Multiple calls to SVC_REQ 57 in a single sweep may cause CPU watchdog timeouts</b>	<p>Multiple calls to SVC_REQ 57 (<i>Logic Driven Write to Nonvolatile Storage</i>) could result in the CPU tripping the watchdog timer and going to STOP-HALT mode. The number of calls to SVC_REQ 57 that can be made requires consideration of many variables, what the software watchdog timeout value is, how much data is being written, how long the sweep is, age of nonvolatile storage (flash), etc. If the application attempts to write to flash too frequently, the CPU could experience a watchdog timeout while waiting for a preceding write operation to complete.</p> <p>The <i>Logic Driven Read/Write to Flash</i> service requests are not intended for high frequency use. GE Intelligent Platforms recommends limiting the number of calls to SVC_REQ 57 to one call per sweep to avoid a potential watchdog timeout and the resulting transition to Stop-Halt.</p>
<b>Error response 1 is no longer returned for Modbus RTU requests with invalid or undefined function codes.</b>	Prior to release 6.70 for the RX3i, the Modbus RTU slave protocol would return an <i>Invalid Function Code</i> error response (1) upon receipt of a request with an invalid or undefined function code. Starting with release 6.70, the Modbus RTU slave ignores requests with an invalid or undefined function code, and no response is sent.
<b>RUN LED is not illuminated on the Series 90-30 power supply for an RX3i remote/expansion rack with input modules only (releases earlier than 6.70).</b>	<p>For firmware version 6.70 and later, the RUN LED for a remote/expansion rack reflects the current IO enable/disable state (even when there are no output modules in the expansion rack).</p> <p>The RUN LED for a remote/expansion rack with only input modules works as follows for all versions prior to version 6.70:</p> <p>When a remote or expansion baseplate is used with the RX3i, the RUN LED on the Series 90-30 power supply for that baseplate is illuminated when the system is in Run mode only if the rack contains at least one output module. If the rack contains input modules only, the RUN LED is not illuminated. This is due to the way input modules are managed in the PACSystems design and does not indicate an error.</p>
<b>Undefined symbols in C blocks</b>	<p>If an attempt is made to download a C block containing undefined symbols, the download will fail. Machine Edition will display the following message in the Feedback Zone: Error 8097: Controller Error – Controller aborted the request [0x05][0xFF]</p> <p>Prior to Release 5.00, C blocks containing undefined symbols could be successfully downloaded, but if they were executed the CPU would transition to Stop/Halt mode.</p> <p>For details, see <i>C Toolkit Compatibility</i>.</p>
<b>Length of serial I/O buffer</b>	The <i>Set Up Input Buffer Function</i> always allocates a buffer containing 2049 bytes. This is one byte more than previous PACSystems releases.
<b>Changing IP Address of Ethernet interface while connected</b>	Storing a hardware configuration with a new IP address to the RX3i while connected via Ethernet will succeed, then immediately disconnect because the RX3i is now using a different IP address than the Programmer. You must enter a new IP address in the Target Properties in the Machine Edition Inspector window before reconnecting.
<b>Duplicate station address for Modbus will conflict with other nodes</b>	The default serial protocol for the RX3i is Modbus RTU. The default Station Address is 1. If the RX3i is added to a multi-drop network, care must be taken that the RX3i is configured with a unique Station Address. Nodes with duplicate Station Addresses on the same network will not work correctly.
<b>Timer operation</b>	<p>Care should be taken when timers (ONDTR, TMR, and OFDTR) are used in program blocks that are NOT called every sweep. The timers accumulate time across calls to the sub-block unless they are reset. This means that they function like timers operating in a program with a much slower sweep than the timers in the main program block. For program blocks that are inactive for large periods of time, the timers should be programmed in such a manner as to account for this catch up feature.</p> <p>Related to this are timers that are skipped because of the use of the JUMP instruction. Timers that are skipped will NOT catch up and will therefore not accumulate time in the same manner as if they were executed every sweep.</p>
<b>Constant sweep</b>	Constant Sweep time, when used, should be set at least 10 ms greater than the normal sweep time to avoid over-sweep conditions when monitoring or performing on-line changes with the programmer. Window completion faults will occur if the constant sweep setting is not high enough.

Operational Notes

Subject	Description
<b>Large number of COMMREQs sent to module in one sweep causes faults</b>	A large number of COMMREQs (typically greater than 8) sent to a given board in the same sweep may cause Module Software faults to be logged in the RX3i fault table. The fault group is MOD_OTHR_SOFTWR (16t, 10h) and the error code is COMMREQ_MB_FULL_START (2). When this occurs, the FT output of the function block will also be set. To prevent this situation, COMMREQs issued to a given board should be spread across multiple sweeps so that only a limited number (typically 8 or less) of COMMREQs are sent to a given board in each sweep. In addition, the FT output parameter should be checked for errors. If the FT output is set (meaning an error has been detected), the COMM_REQ could be re-issued by the application logic.
<b>C Block standard math functions do not set errno</b>	In C Blocks, standard math functions (e.g. sqrt, pow, asin, acos) do not set errno to the correct value and do not return the correct value if an invalid input is provided.
<b>Upgrading firmware</b>	<ul style="list-style-type: none"> <li>▪ Upgrading the CPU firmware with the WinLoader utility may fail when multiple IO modules are in the main rack, due to the time it takes to power cycle the rack system. If the upgrade process fails, move the CPU to a rack without IO modules and restart the upgrade process.</li> <li>▪ Winloader initial connect baud rate is fixed at 19200 baud. Note that the firmware download will occur at 115.2K baud by default.</li> <li>▪ Note that if you have Hyperterm open on a port, and then try to use Winloader on the same port, Winloader will often say <i>Waiting for Target</i> until the hyperterm session is closed.</li> </ul>
<b>Hot swap</b>	Hot Swap of power supplies or CPUs is not supported.
<b>Serial port configuration COMMREQs</b>	<p>With the following combination of circumstances, it is possible to render serial communications with the CPU impossible:</p> <ul style="list-style-type: none"> <li>▪ User configuration disables the Run/Stop switch</li> <li>▪ User configures the power up mode to Run or Last</li> <li>▪ Logic is stored in FLASH and user configures CPU to load from FLASH on power-up</li> <li>▪ User application issues COMMREQs that set the protocol on both of the serial ports to something that does not permit communications to the Machine Edition programmer.</li> </ul>
<b>Incorrect COMMREQ status for invalid program name</b>	The program name for PACSystems is always <i>LDPROG1</i> . When another program name is used in a COMM_REQ accessing %L memory, an Invalid Block Name (05D5) error is generated.
<b>FANUC I/O Master and Slave operation</b>	<p>Scan sets on the master do not work properly for the first operation of the scan set after entering RUN mode. They do work properly for subsequent scans.</p> <p>After downloading a new hardware configuration and logic, a power cycle may be required to resume FANUC I/O operation.</p> <p>Use controllers of similar performance in FANUC I/O networks. If a master or slave is located in an RX3i system, the other controllers should be RX3i or Series 90-30 CPU374.</p> <p>Repeated power up/down cycles of an expansion rack containing FANUC I/O slaves may result in failure of the slaves' operation, with the RDY LED off.</p>
<b>Lost count at power up for Serial IO Processor</b>	The serial IO Processor (IC693APU305) will lose the first count after every power up or every time the module receives a configuration.
<b>COMMREQ status words declared in bit memory types must be byte-aligned.</b>	<p>In previous releases, the CPU allowed configuration of COMMREQ Status Words in bit memory types on a non-byte-aligned boundary. Even though the given reference was not byte-aligned, the firmware would adjust it the next-lowest byte boundary before updating status bits, overwriting the bits between the alignment boundary and specified location.</p> <p>To ensure that the application operates as expected, release 3.50 requires configuration of COMMREQ Status Words in bit memory types to be byte-aligned. For example if the user specified status bit location of %I3, the CPU aligns the status bit location at %I1. Release 3.50 firmware requires the user to specify the appropriate aligned address (%I1) to ensure that the utilized location is appropriate for their application. Note that the actual reference location utilized is not changed, but now is explicitly stated for the user.</p>

**Operational Notes**

Subject	Description
<p><b>STOP and RUN mode transition priority</b></p>	<p>The PACSystems CPU receives requests to change between STOP and RUN mode from many different sources. These include (but are not limited to) Proficy Machine Edition, HMIs, the user application, and the RUN/STOP switch. Since there are many potential sources for a mode change request, it is possible to receive a new mode change request while another is already in progress. When this occurs, the CPU evaluates the priority of the new mode change request with the mode change that is in progress. If the new mode change request has an equal or higher priority than the one already in progress, the CPU transitions to the new mode instead of the one in progress. If, however, the new mode change request has a lower priority than the one in progress, the new mode request is discarded and the CPU completes the mode change that is in progress. The sweep mode priorities are listed from highest to lowest priority: STOP HALT, STOP FAULT, STOP, and RUN. (Note: The IO ENABLED/DISABLED state is not part of the mode priority evaluation.) For example, a CPU is in RUN IO ENABLED mode and a Service Request 13 function block is executed to place the CPU into STOP IO DISABLED mode. Before the transition to STOP IO DISABLED is completed, the RUN/STOP switch is changed from RUN IO ENABLED to RUN IO DISABLED. In this case, the CPU ignores the new request from the RUN/STOP switch to go to RUN IO DISABLED mode because it is already processing a request to go to STOP IO DISABLED mode and STOP mode has a higher priority than RUN mode.</p>
<p><b>Suspend IO function block does not suspend EGD</b></p>	<p>In a Series 90-70 the SUSPEND_IO function block suspends EGD in addition to IO Scan. In PACSystems controllers the SUSPEND IO suspends only the IO Scan.</p>

**Additional Information**

For additional information, please refer to the manuals listed below. Manuals can be downloaded from the support website, <http://support.ge-ip.com>.

<i>PACSystems RX7i &amp; RX3i CPU Reference Manual</i>	GFK-2222
<i>PACSystems RX7i &amp; RX3i CPU Programmer's Reference Manual</i>	GFK-2950
<i>PACSystems Hot Standby CPU Redundancy User's Manual</i>	GFK-2308
<i>PACSystems Memory Xchange Modules User's Manual</i>	GFK-2300
<i>C Programmer's Toolkit for PACSystems</i>	GFK-2259
<i>PACSystems RX3i System Manual</i>	GFK-2314
<i>PACSystems RX7i &amp; RX3i TCP/IP Ethernet Communications User Manual</i>	GFK-2224
<i>PACSystems TCP/IP Ethernet Communications Station Manager User Manual</i>	GFK-2225
<i>PACSystems RXi, RX3i, and RX7i Controller Secure Deployment Guide</i>	GFK-2830
<i>PACSystems RX3i and RX7i Battery and Energy Pack Manual</i>	GFK-2741
<i>PACSystems RX3i PROFINET IO Controller User Manual</i>	GFK-2571
<i>PACSystems RX3i Ethernet NIU User's Manual</i>	GFK-2439
<i>VersaMax PROFINET Scanner Manual</i>	GFK-2721
<i>PACSystems RX3i HART Pass Through User Manual</i>	GFK-2929

