Bus Expansion Modules

IC687BEM742, IC687BEM744

GFK-1450A
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FIP Bus Controller

Features

- Interfaces FIP or World FIP I/O serial bus to IC697 PLC
- Two versions available for installation in IC697 VME Integrator racks IC687BEM742 and IC687BEM744 - both have 2M of RAM and 2M of Flash memory
- Data rate for IC687BEM742 is 1 Mbit/second, IC687BEM744 is 2 5 Mbits/second
- Four FIP Bus Controllers per PLC system
- Two FIP bus channels provide redundant bus capability
- RS-485 serial port attaches to PC for easy in-system firmware upgrade (no PROMS to change)
- Pushbutton for resetting Bus Controller and enabling Bus Controller to accept upgrades
- FIP bus faults managed by PLC Alarm processor Function
- Six status LEDs
- Software configuration (no DIP switches or jumpers to set)

Functions

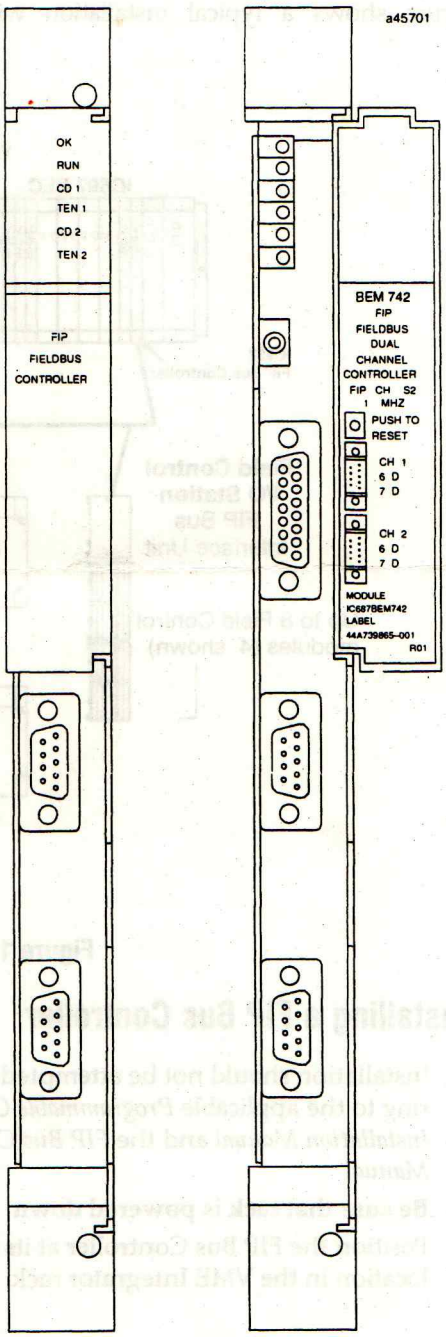
This FIP Bus Controller (FBC) is a two channel bus controller that occupies a single half-slot in an IC697 PLC VME Integrator rack. The FBC is configured with Windows® based programming software I/O devices on the FIP bus are scanned asynchronously by the bus controller; I/O data is transferred to the CPU once per scan

Up to 31 Bus Controllers, of any kind, can be included in an IC697 PLC system Of the 31 Bus Controllers, a maximum of four can be FIP Bus Controllers

A FIP bus may serve:

- **IC697 and IC693 PLCs** interfaced to the bus by FIP Bus Controllers
- **Remote Drops**, IC693 I/O racks that are interfaced to the bus through Remote I/O Scanner Modules Each remote drop can include any mix of discrete and analog I/O modules
- **Field Control Stations**, Field Control I/O modules that are interfaced to the bus via a FIP Bus Interface Unit (BIU)
- **Generic Devices**, such as general-purpose computers that are interfaced to the bus via a 3rd Party FIP Module

A FIP bus is used primarily for I/O control It is also used to store configuration data to remote devices and to report faults



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